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Case Docket No. YKI-0024

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Sir:

Transmitted herewith for filing is the patent application of :

INVENTOR(S): Ryoichi Yokoyama

FOR: ACTIVE-TYPE ELECTROLUMINESCENT DISPLAY

Enclosed are:

☒ 18 pages of specification.

☒ 16 claims.

☒ 4 sheet(s) of drawing(s).

☒ Declaration and Power of Attorney

☐ Information Disclosure Statement.

☐ An associate power of attorney.

☒ An assignment of the invention to Sanyo Electric Co., Ltd.

☐ A certified copy of a _____ application.

☐ A verified statement to establish small entity under 37 CFR 1.9 and 37 CFR 1.27.

The filing fee has been calculated as shown below:

			SMALL ENTITY		LARGE ENTITY	
FOR:	# FILED	# EXTRA	RATE	FEE	RATE	FEE
BASIC FEE	////////	////////	\$380		\$760	
TOTAL CLAIMS (20)	16	0	X 9		x 18	
INDEP. CLAIMS (3)	2	0	x 39		x 78	
MULTIPLE DEPENDENT CLAIMS			+130		+260	
			TOTAL		TOTAL	760.00

jc688 U.S. PTO

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Please file this application and conduct all future correspondence with Applicant's attorney identified below.

Respectfully Submitted,

Ryoichi Yokoyama

CANTOR COLBURN
Applicant's Attorneys

By: 

Edward J. Ellis

Registration No. 40,389

Date: November 29, 1999

Address: 88 Day Hill Road, Windsor, CT 06095

Telephone: (860) 688-4470

ACTIVE-TYPE ELECTROLUMINESCENT DISPLAY**BACKGROUND OF THE INVENTION**

Field of the Invention:

5 The present invention relates to an active-type display wherein luminescent elements, e.g. electroluminescent (EL) elements or fluorescent elements are driven with thin film transistors (TFTs).

Description of Related Art:

10 Organic EL elements are self-emitting displays. The organic EL element does not require any backlight indispensable for liquid-crystal displays and is suitable for thinning the display structure. Since the viewing angle of the organic EL element is not limited, the organic EL element has been highly
15 expected to be put to practical use as the next-generation display.

20 In order to form an organic EL panel 1, as shown in Fig. 1, an anode electrode 3 formed of e.g. an ITO transparent electrode is formed on a transparent glass substrate 2. A hole-transport layer 5 of MTDATA (4,4'-bis(3-methylphenylphenylamino)biphenyl), an emissive layer 6 of TPD (4,4',4''-tris(3-methylphenylphenylamino)triphenylamine) and Rubrene, and an electron-transport layer 7 of Alq₃ are sequentially deposited between the anode electrode 3 and a
25 cathode electrode 4 of MgIn alloy. When holes injected from the anode electrode 3 and electrons injected from the cathode electrode 4 are recombined within the emissive layer 6, light is emitted out from the transparent anode.

 The display which drives the organic EL elements is

classified into two types: a passive type using a passive matrix and an active type using TFTs. The schematic configuration of the passive type display is shown in Fig. 2.

5 A pair of electrodes in an EL element includes an anode electrode 3 in column and a cathode electrode 4 in row. Anode electrodes and cathode electrodes are arranged in a matrix. A row driver 8 supplies scanning signals ROW1, ROW2, ROW3, ... to the cathode electrodes 4 respectively to select one of plural rows. The column driver 9 receives gray scale (tone) data
10 representing a display gray scale of each pixel and then outputs pulse signals with a pulse width corresponding to the gray scale data, as column drive signals COL1, COL2, COL3, ..., COLm. Thus, the EL elements emit light, corresponding to pixels PX1, PX2, PX3, ..., PXm to which scanning signals and
15 column drive signals are supplied.

Usually, in order to apply a predetermined positive voltage to the anode electrodes and to apply the ground potential or negative voltage to the cathode electrodes, a signal input substrate 10 such as TAB or FPC is connected to the organic EL
20 panel 1. As shown in Fig. 1, copper connection terminals 11 are formed on the back surface of the signal input substrate 10 to supply predetermined voltages to the corresponding cathode electrodes. Conventionally, to connect the connection
25 terminal 11 to the corresponding cathode electrode 4, a cathode material is extended to the connection terminal without any change.

In the passive-type display, electrons from EL elements corresponding to all pixels in the same column sink into the cathode electrodes. In the active-type display using TFT

transistors, a sole cathode electrode is formed as a common electrode over the whole of the panel display area. All currents, which are emitted from EL elements corresponding to all pixels within a display area, are sank into the cathode. This current value becomes very large. Hence, like the conventional passive-type display, if the cathode material, in the active-type display, is extended from the cathode electrode to the panel terminal, as the connection conductor to the connection terminal of the signal input substrate, the connection conductor is narrowed significantly, compared with the cathode electrode. As a result, a large current from the cathode electrode becomes concentrated in the connection conductor. The cathode electrode made of Mg alloy has a relatively large resistance value. The large resistance component of the connection conductor causes a large voltage drop, thus decreasing the intensity of an EL element. This problem becomes more serious as the display screen is enlarged.

SUMMARY OF THE INVENTION

This invention is made to overcome the above-mentioned problems. It is an object of the present invention to provide a luminescent display which has connecting conductors of a reduced resistance value, thus providing an improved luminous intensity.

In one aspect of the present invention, an active-type light emission display comprises a light emission panel including light emission elements each having a first electrode, a second electrode, and an emissive portion, and thin-film transistors for respectively driving the light emission

elements; and connection conductors each for connecting the second electrode and a signal supply portion, the signal supply portion supplying signals to the second electrode. The connection conductors each have a section between the second electrode and the signal supply unit. At least a part of the section is a multilayer structure formed of a second electrode material used for the second electrode and an conductive material used for the thin film transistors.

The multilayer structure of the connection conductor allows the cross section thereof to be substantially increased, without spreading the horizontal area of the connection conductor, thus decreasing the electrical resistance value. The lowered conductor resistance enables a small drop in a signal voltage applied to the second electrode. The signal can be effectively applied to the cathode so that a light emission element, for instance, an EL element, can be made to emit light at a high intensity.

In another aspect of the invention, the connection conductor does not have a multilayer structure formed with the second electrode. Instead, in the connection conductor, at least a part of the section between the second electrode and the signal supply portion is formed using only the conductive material for thin film transistors.

The conductive material used for the thin film transistor is, for example, a material used for the gate electrode, the drain electrode, and the source electrode of a transistor, generally, a low resistance material. Using the conductive material as a connection conductor material allows the electrical resistance of the connection conductor to be sufficiently reduced, so

that the EL element can be made to emit light at a high intensity.

In another aspect of the present invention, the conductive material used for thin film transistors and connection conductors comprises a material used for a gate electrode, a drain electrode, or a source electrode of each of the thin film transistors, or comprises an arbitrary combination of materials used for the gate electrode, the drain electrode and the source electrode thereof.

The conductive material used for the thin film transistors and the connection conductors comprises a metal material used for a gate electrode or drain electrode of each of the thin film transistors.

The conductor resistance can be sufficiently lowered by using a low resistance material, such as a metal used for electrodes of a thin film transistor, as the material for the connection conductor (including a multilayer structure laminated with a second electrode material).

Furthermore, either a multilayer structure of a second electrode material and a conductive material for a thin film transistor or a conductive material used for a thin film transistor is used as the connection conductor. Thus, the connection conductor can be formed at the same time in the thin film transistor and/or cathode forming steps. Hence, a low resistance connection conductor can be obtained without adding a special step for the connection conductor.

In another aspect of the invention, the second electrode acts as a common electrode spreading all over the panel. Since the second electrode acting as a common electrode receives a

large current, a large voltage drop fixed as the product of the conductor resistance and above large current arises so that the intensity of the light emission decreases. However, the above-mentioned multilayer structure can reduce the conductor resistance.

In yet another aspect of the present invention, the light emission element comprises an electroluminescent element in which an emissive layer is interposed between the first electrode and the second electrode. The emissive layer includes an organic compound with a luminous function. That is, the present invention is applicable to organic EL elements. In a current drive-type organic EL element, the present invention can reduce the resistance of the connection conductor which connects the cathode into which a large current is sunk and a signal supply portion for supplying signals to the cathode electrode. For that reason, the organic EL element can emit light at a high intensity.

In another aspect of the invention, the active layer of a thin film transistor comprises a polycrystalline silicon film. The polycrystalline silicon film improves the operational rate of a thin film transistor and allows a thin film transistor to be fabricated in a small area. As a result, a high-resolution display, for instance, EL display, vacuum fluorescent display (VFD), or field emission display (FED), can be obtained.

In still another aspect of the present invention, the signal supply portion comprises an external device connected to the light emission panel via the terminals. Generally, since the connection conductor connected to the external device is formed in as small an area as possible, the resistance value

thereof tends to increase. Even in such a case, the present invention can sufficiently reduce the electrical resistance of the connection conductor.

5 BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects, features, and advantages of the present invention will become more apparent upon a reading of the following detailed description and drawings, in which:

10 Fig. 1 is a cross-sectional view schematically illustrating a conventional passive-type EL display;

Fig. 2 is a schematic circuit configuration of a conventional passive-type EL display;

15 Fig. 3 is a circuit configuration for each pixel in an active-type EL display according to an embodiment of the present invention;

Fig. 4 is a cross-sectional view schematically illustrating a circuit configuration for each pixel in an active-type EL display according to an embodiment of the present invention;

20 Fig. 5 is a cross-sectional view schematically illustrating an active-type EL display according to an embodiment of the present invention;

Fig. 6 is a plan view schematically illustrating an active-type EL display according to an embodiment of the present invention;

25 Fig. 7 is a cross-sectional view schematically illustrating an active-type EL display, different from that of Fig. 5, according to another embodiment of the present invention; and

Fig. 8 is a plan view schematically illustrating an active-type EL display, different from that of Fig. 6, according to

another embodiment of the present invention.

DESCRIPTION OF THE EMBODIMENTS

5 An EL display according to the present invention is an active-type EL display. The configuration of each pixel is shown in Fig. 3.

10 A pixel consists of an organic EL element 20, a first switching TFT 21, a capacitor 22, and a second TFT 23. The first TFT 21 is turned on/off by a selection signal Scan and a display signal Data is applied its drain. The capacitor 22 is electrically charged by the display signal Data supplied when the TFT 21 is turned on and holds the charged voltage V_h when the TFT 21 is turned off. The second TFT 23 has a drain connected to the drive power supply COM and a source connected to an anode electrode and drives the organic EL element 20 when the holding voltage V_h of the capacitor 22 is applied to the gate.

15 When the TFT 21 is turned on with the selection signal Scan being at an H level during a selected horizontal scanning period (1H), the display signal Data is supplied to the capacitor 22. Thus, the capacitor 22 is charged to the voltage V_h corresponding to the display signal Data. Even when the TFT 21 is turned off by the signal Scan being at an L level, the capacitor 22 continues to hold the voltage V_h during one vertical scanning period (1V). Since the voltage V_h is supplied to the gate of the TFT 23, the drive voltage V_{COM} supplies power (current) according to the voltage V_h , to the EL element via the TFT 23. Thus, the EL element is controlled to emit light at an intensity corresponding to the supplied

current.

Fig. 4 is a diagram illustrating the configuration including the organic EL element 20 and the TFT 23 in Fig. 3.

The organic EL element 20 is fabricated by sequentially laminating a hole-transport layer 52 including MTDATA, an emissive layer 53 including TPD and Rubrene, and an electron-transport layer 54 including Alq_3 , between an anode 51 made of transparent ITO and a cathode 55 made of MgIn alloy. When holes injected from the anode 51 and electrons injected from the cathode 55 are recombined within the emissive layer 53, light is emitted. The light, shown with the arrow in Fig. 4, is emitted from the transparent anode. It should be noted that the material forming the organic EL element 20 is not limited only to that in the present embodiment.

The drive TFT 23 is fabricated by sequentially depositing a gate electrode 61 made of chromium Cr, a gate insulating film 62, a polycrystalline silicon film 65 including a drain region 63 and a source region 64, an interlayer insulating film 66, and a planarized insulating film 67, on a glass substrate 60. The drain region 63 is connected to the drain electrode 68 made of aluminum Al. The source region 64 is connected to the ITO 52 acting as the anode of the organic EL element 20 and forming the source electrode of the TFT 23. It should be noted that the switching transistor is not limited to only the TFT including the active layer made of the polycrystalline silicon film. For instance, the polycrystalline silicon film enables to fabricate high-speed, small TFTs, thus resulting in a high resolution display.

In the embodiment of the invention, the pixels are arranged

in a matrix to fabricate an active-type EL panel 30. Fig. 5 shows the cross section of the connection between the panel 30 and the signal input substrate (TAB or FPC) 35. Fig. 6 shows the plan view of the same connection.

5 Referring to Fig. 5, the EL panel 30 is fabricated by sequentially laminating a hole-transport layer 52, an emissive layer 53, an electron-transport layer 54, and a cathode layer 55, on the ITO anode 51. The layer 31 corresponds to an insulating layer which includes a gate insulating film 62, an
10 interlayer insulating film 66, and a planarized insulating film 67, formed on the substrate 60, shown in Fig. 4. Similarly, the layer 32 corresponds to a metal layer which includes a chromium Cr acting as the gate electrode and a metal layer made of aluminum Al, shown in Fig. 4. A connection
15 terminal 36 is formed on the back surface of the signal input substrate 35 to supply a predetermined voltage to the cathode 55.

As shown in Fig. 6, the cathode 55 acts as a common electrode to all pixels extending over the whole display area. The
20 multilayer structure formed of the cathode material layer 55 and the conductive material layer 33, shown in Figs. 5 and 6, acts as a conductor line to connect the cathode 55 to the signal input terminal 36 of the signal input substrate 35. The conductive material used for the TFT 21 and/or 23 is used as
25 the conductive material layer 33. More specifically, ITO is used for the source electrode (pixel electrode) 51 of a TFT and for the anode acting as the anode 51 of the EL element 20. Chromium Cr is used for the gate electrode 61. Aluminum Al is used for the drain electrode 68. Moreover, a combination of

ITO, Chromium, and aluminum may be used for the conductive layer 33.

5 The conductive material used in the TFT fabrication process has a much low resistance value than cathode materials including MaIn alloy, MaAg alloy, and a laminated material of LiF and Al. Hence, these conductive materials can reduce the conductor resistance outside the display area, thus reducing a decrease in display intensity. Particularly, the metal materials such as chromium Cr, aluminum Al and the like, which
10 have a considerably lower resistance than ITO, can provide significant effects. These materials, which are used in the ITO processing step, can be handled without increasing the number of steps.

15 In the above-mentioned embodiment, the multilayer structure made of the cathode material and the conductive material used in the TFT fabrication step is used as a conductor for connecting the cathode 55 to the connection terminal 36. Furthermore, another embodiment may have the following multilayer structure.

20 Fig. 7 is a cross sectional view illustrating a display panel according to another embodiment. Fig. 8 is a plan view illustrating the display panel shown in Fig. 7. The conductive material layer 34 is in contact with the cathode 55. The conductive material 34 is extended over the substrate 60 to form a conductor to the connection terminal 36. As described
25 with the conductive material 33, the conductive material layer 34 is formed of either ITO, chromium Cr, or aluminum Al which are used for the source electrode (the anode of an EL element), the drain electrode, or the gate electrode of the TFT 21 or 23,

or of a combination of them. Like the first embodiment, that structure can maintain the resistance of a conductor extending outside the display area at a low value. As a result, a decrease in display intensity can be suppressed. Particularly, metal materials including chromium Cr, aluminum Al, and the equivalents can effectively reduce the resistance component. In the TFT forming process, the conductive material layer 34 can be formed together with electrodes by performing photolithography so that a special step is not required and the connection terminal can be formed with a high precision.

In the active-type EL display according to the present invention, the resistance value of a connector lying outside the display area can be suppressed low so that a decrease in display intensity can be decreased.

In the organic EL element of the present invention wherein an organic compound is used for the emissive layer 53, the hole-transport layer 52, and the electron-transport layer 54, the organic compound layer usually has a dielectric property. Hence, when the connection conductor (cathode) disposed between the connection terminal 36 and the cathode, as shown in Figs. 5 and 7, is formed on the side of an organic EL element, the connection conductor is electrically isolated from the anode 51 or the electrode layer 32 of a TFT. However, the method of electrically isolating the organic EL element from the connection conductor is not limited only to the above-mentioned method. Normally available isolating methods are also applicable for this invention.

The organic EL display adopting organic EL elements has been explained by referring to the above embodiments. However, it

should be noted that the present invention is not limited only to the embodiments. The present invention is also applicable to light emission displays including vacuum fluorescent displays (VFD), field emission displays, and their equivalents.

5 Thus, the above-mentioned technique can be adopted to reduce the resistance of the second electrode (e.g. cathode) conductor.

WHAT IS CLAIMED IS:

1. An active-type light emission display comprising:

5 a light emission panel including light emission elements each having a first electrode, a second electrode, and an emissive portion, and thin-film transistors for respectively driving said light emission elements; and

10 connection conductors each for connecting said second electrode and a signal supply portion, said signal supply portion supplying signals to said second electrode;

15 said connection conductors each having a section between said second electrode and said signal supply unit, at least a part of said section being a multilayer structure formed of a second electrode material used for said second electrode and an conductive material used for said thin film transistors.

20 2. The active-type light emission display defined in Claim 1, wherein said conductive material used for said thin film transistors and said connection conductors comprises a material used for a gate electrode, a drain electrode, or a source electrode of each of said thin film transistors, or comprises an arbitrary combination of materials used for said gate electrode, said drain electrode and said source electrode thereof.

25

3. The active-type light emission display defined in Claim 1, wherein said conductive material used for said thin film transistors and said connection conductors comprises a metal material used for a gate electrode or drain electrode of each of said thin film transistors.

4. The active-type light emission display defined in Claim 1, wherein said second electrode comprises a common electrode spreading over substantially the whole of said panel.

5. The active-type light emission display defined in Claim 1, wherein said light emission element comprises an electroluminescent element in which an emissive layer is interposed between said first electrode and said second electrode.

6. The active-type light emission display defined in Claim 5, wherein said emissive layer comprises an organic compound having a luminous function.

7. The active-type light emission display defined in Claim 1, wherein an active layer of each of said thin film transistors comprises a polycrystalline silicon layer.

8. The active-type light emission display defined in Claim 1, wherein said signal supply portion comprises an external device connected to said light emission panel

via terminals.

9. An active-type electroluminescent display comprising:
a light emission panel including light emission
elements each having a first electrode, a second
electrode, and a luminous portion, and thin-film
transistors for respectively driving said light
emission elements; and

connection conductors each for connecting said
second electrode and a signal supply portion, said
signal supply portion supplying signals to said
second electrode;

said connection conductors each having a section
between said second electrode and said signal
supply portion, at least a part of said section
being formed of a conductive material used for
said thin film transistors.

10. The active-type electroluminescent display defined in
Claim 9, wherein said conductive material used for said
thin film transistors and said connection conductors
comprises a material used for a gate electrode, a drain
electrode, or a source electrode of each of said thin
film transistors, or comprises an arbitrary combination
of materials used for said gate electrode, said drain
electrode and said source electrode thereof.

11. The active-type electroluminescent display defined in
Claim 9, wherein said conductive material used for said thin

film transistors and said connection conductors comprises a metal material used for a gate electrode or drain electrode of each of said thin film transistors.

5 12. The active-type electroluminescent display defined in Claim 8, wherein said second electrode comprises a common electrode spreading over substantially the whole of said panel.

10 13. The active-type electroluminescent display defined in Claim 9, wherein said light emission element comprises an electroluminescent element in which an emissive layer between said first electrode and said second electrode.

15 14. The active-type electroluminescent display defined in Claim 13, wherein said emissive layer comprises an organic compound having a luminous function.

20 15. The active-type electroluminescent display defined in Claim 9, wherein an active layer of each of said thin film transistors comprises a polycrystalline silicon layer.

25 16. The active-type electroluminescent display defined in Claim 9, wherein said signal supply portion comprises an external device connected to said light emission panel via terminals.

ABSTRACT OF THE DISCLOSURE

In an active-type electroluminescent (EL) display, a conductor interconnecting a cathode (55) of an EL panel (30, 40) and a connection terminal of a signal input substrate (35) has a multilayer structure formed of a cathode material and a conductive material used in a thin-film transistor forming step. The conductor may be formed of a conductive material used in a thin film transistor forming step. A metal material for a gate electrode or drain electrode is preferably used as the conductive material. The connection conductor structure can reduce the electrical resistance of the connection conductor, thus preventing a decrease in display intensity of an EL display.

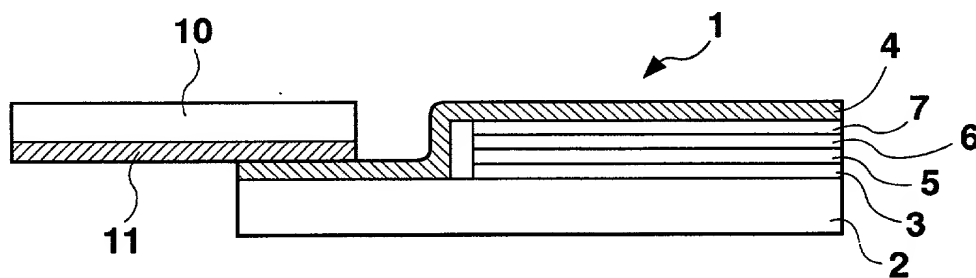


Fig. 1 PRIOR ART

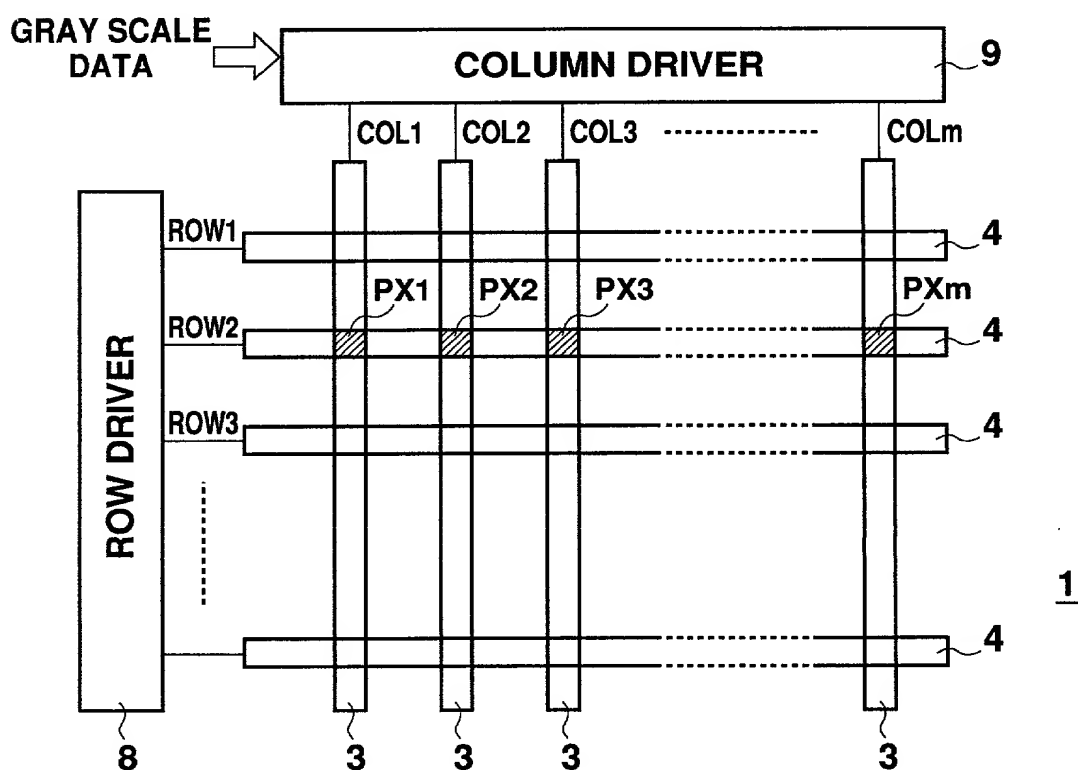


Fig. 2 PRIOR ART

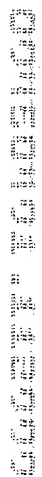


Fig. 4



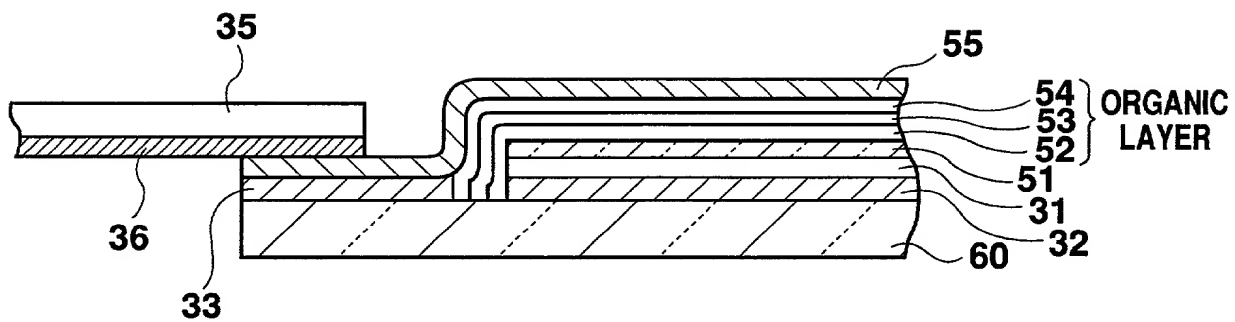


Fig. 5

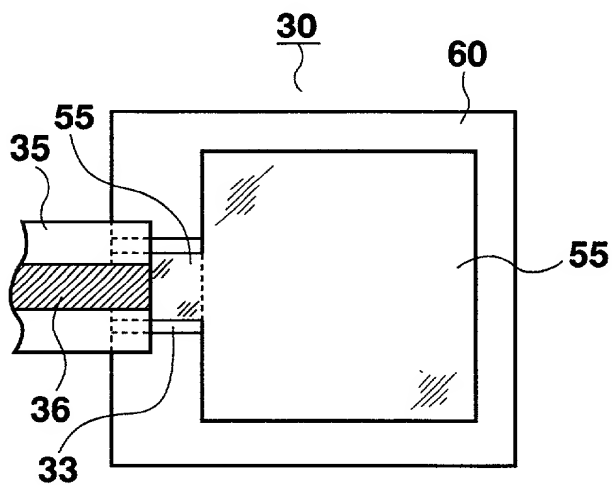


Fig. 6

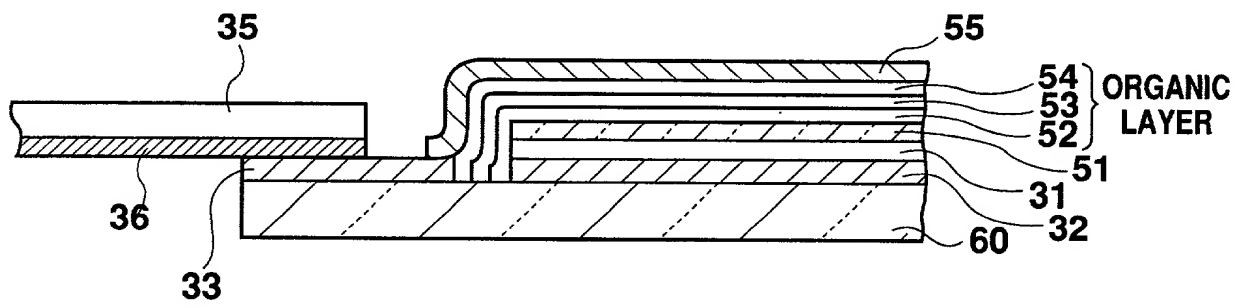


Fig. 7

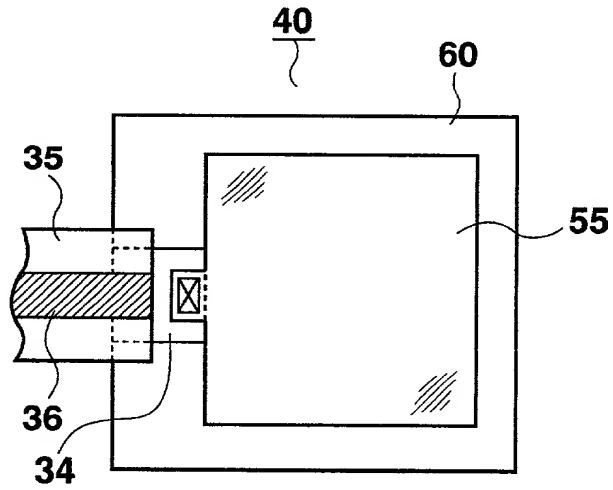


Fig. 8

Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

ACTIVE-TYPE ELECTROLUMINESCENT DISPLAY

上記発明の明細書（下記の欄でx印がついていない場合は、本表に添付）は、

the specification of which is attached hereto unless the following box is checked:

☐ __月__日に提出され、米国出願番号または特許協定条約国際出願番号を____とし、
（該当する場合）____に訂正されました。

☐ was filed on _____
as United States Application Number or
PCT International Application Number
_____ and was amended on
_____ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration
(日本語宣言書)

私は、米国法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、米国以外の国の少なくとも一ヶ国を指定している特許協力条約365(a)項に基づき国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)

外国での先行出願

Hei 10-340501

(Number)
(番号)

Japan

(Country)
(国名)

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Not Claimed

優先権主張なし

30/November/1998

(Day/Month/Year Filed)
(出願年月日)

☐

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

(Number)
(番号)

(Country)
(国名)

(Day/Month/Year Filed)
(出願年月日)

☐

私は、第35編米国法典119条(e)項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

(Application No.)
(出願番号)

(Filing Date)
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)
(出願番号)

(Filing Date)
(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条(c)に基づき権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

(Application No.)
(出願番号)

(Filing Date)
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

(Application No.)
(出願番号)

(Filing Date)
(出願日)

(Status: Patented, Pending, Abandoned)
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration (日本語宣言書)

委任状： 私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁護士または代理人として、下記の者を指名いたします。(弁護士、または代理人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

Michael A. Cantor, Registration No. 31,152; Philmore H. Colburn II, Registration No. 35,101; Keith J. Murphy, Registration No. 33,979; David A. Fox, Registration No. 38,307; Edward J. Ellis, Registration No. 40,389; Robert D. Crawford, Registration No. 38,119

書類送付先

Send Correspondence to:

Michael A. Cantor, Esq.
CANTOR COLBURN LLP
88 Day Hill Road
Windsor, CT 06095 U.S.A.

直接電話連絡先： (名前及び電話番号)

Direct Telephone Calls to: (name and telephone number)

Edward J. Ellis, Esq.
(860) 688-4470

唯一または第一発明者名	Full name of sole or first inventor Ryoichi YOKOYAMA		
発明者の署名	日付	Inventor's signature <i>Ryoichi Yokoyama</i>	Date Nov. 16, 1999
住所	Residence Ohgaki-shi, Gifu, Japan		
国籍	Citizenship Japan		
私書箱	Post Office Address 5-125-176, Ayano, Ohgaki-shi, Gifu, Japan		
第二共同発明者	Full name of second joint inventor, if any		
第二共同発明者	日付	Second inventor's signature	Date
住所	Residence		
国籍	Citizenship		
私書箱	Post Office Address		